

FIG.3

↓ CHECKPOINTS OF A TOWARD B ↑ CHECKPOINTS OF B TOWARD A

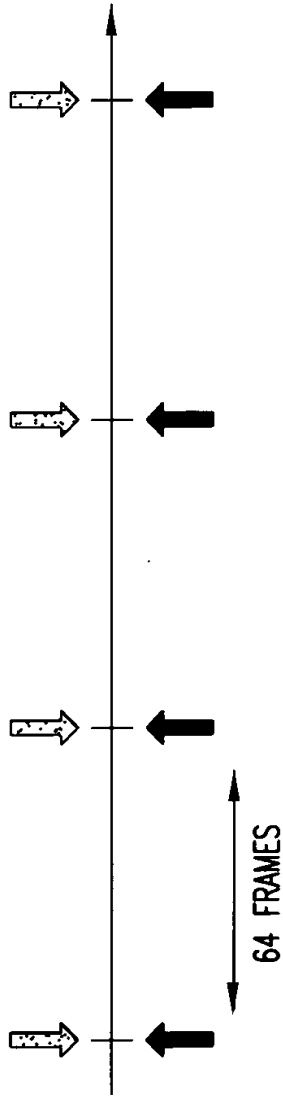


FIG. 4(a)

↓ CHECKPOINTS OF A TOWARD B ↑ CHECKPOINTS OF B TOWARD A

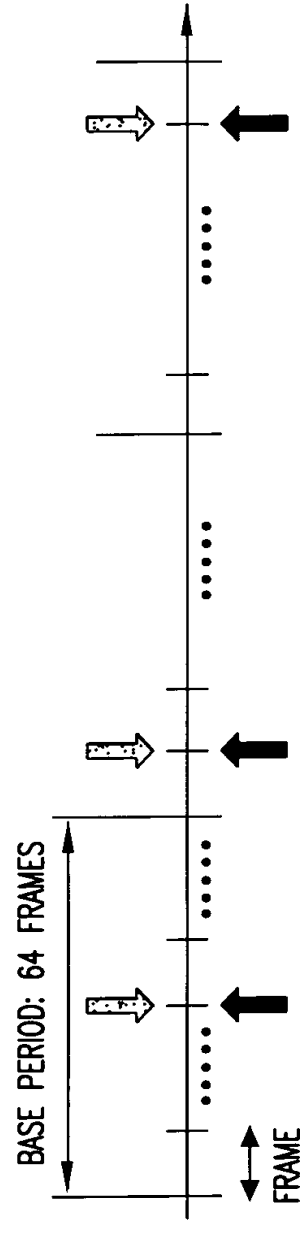


FIG. 4(b)

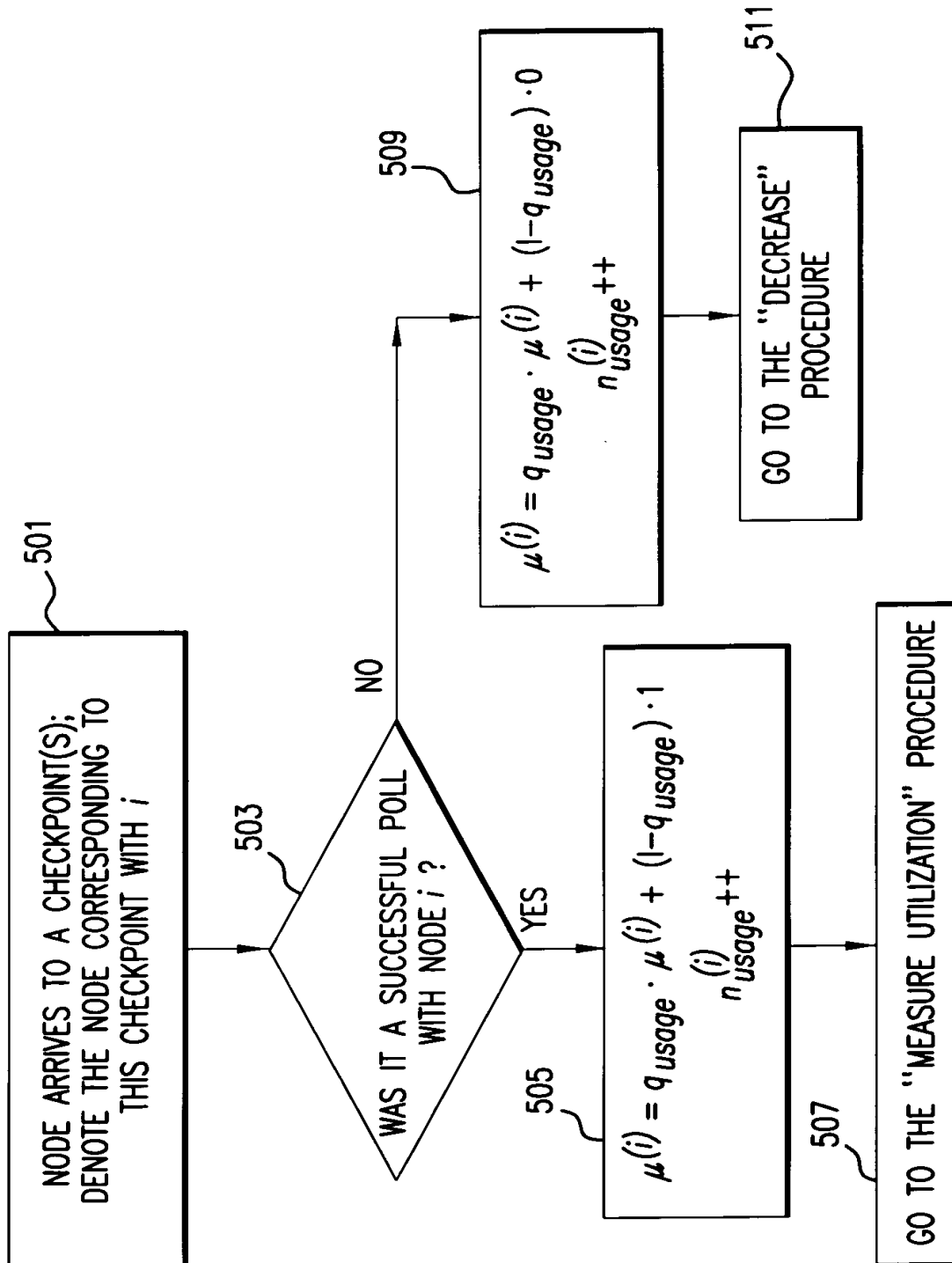


FIG. 5

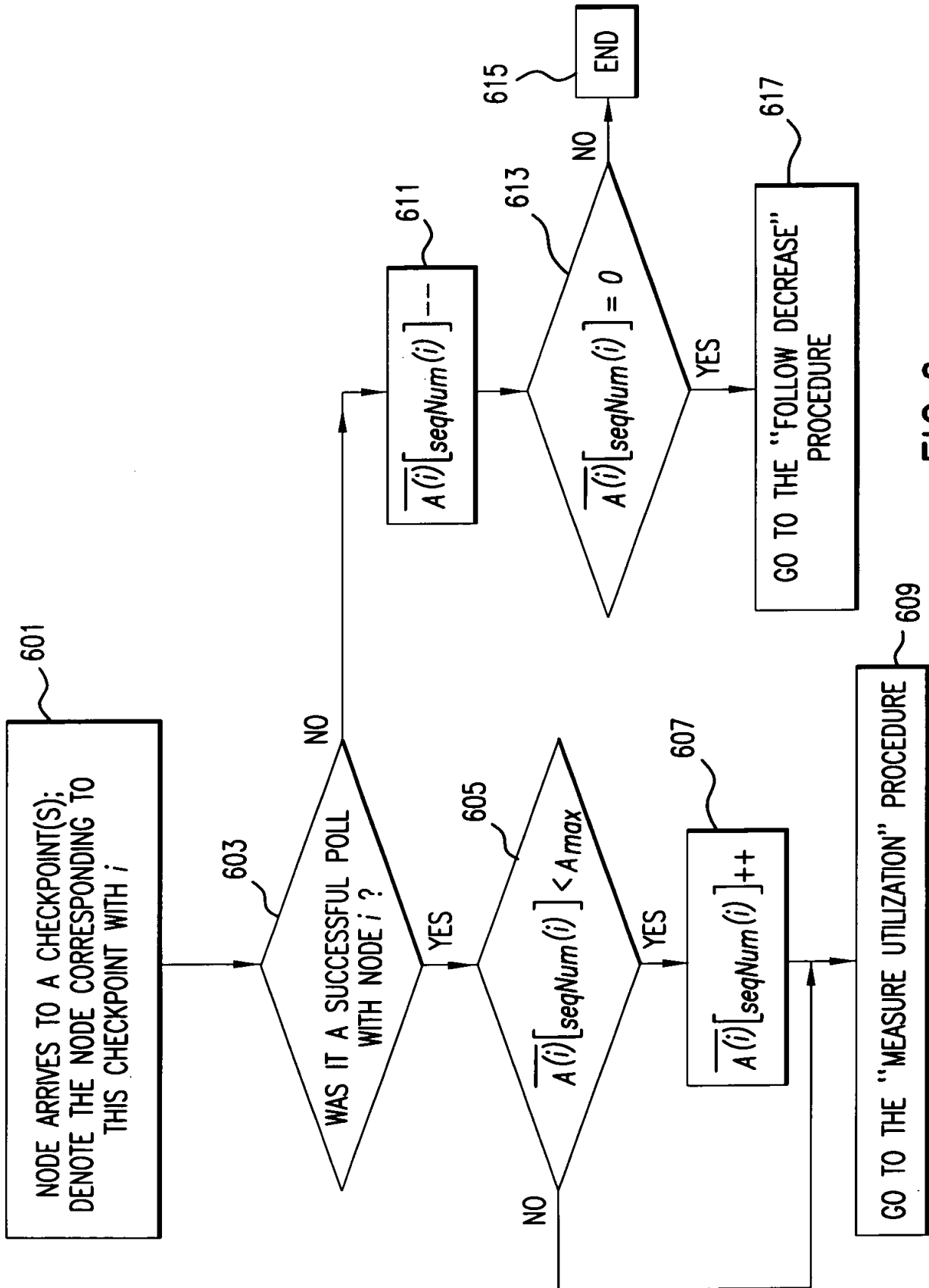


FIG. 6

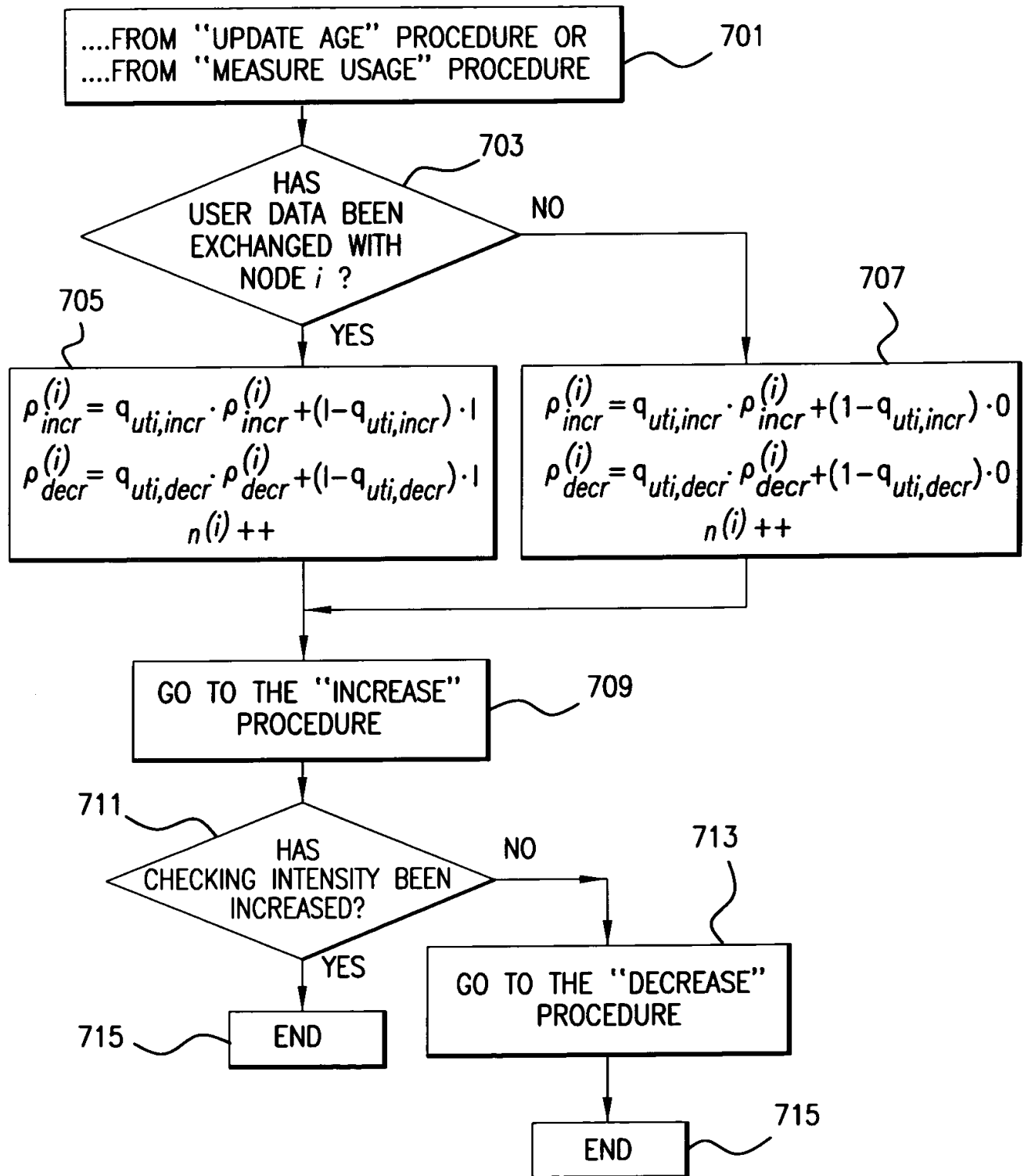


FIG.7

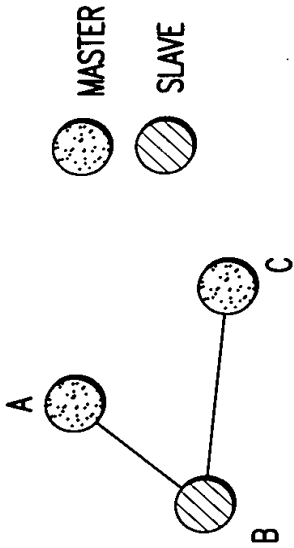
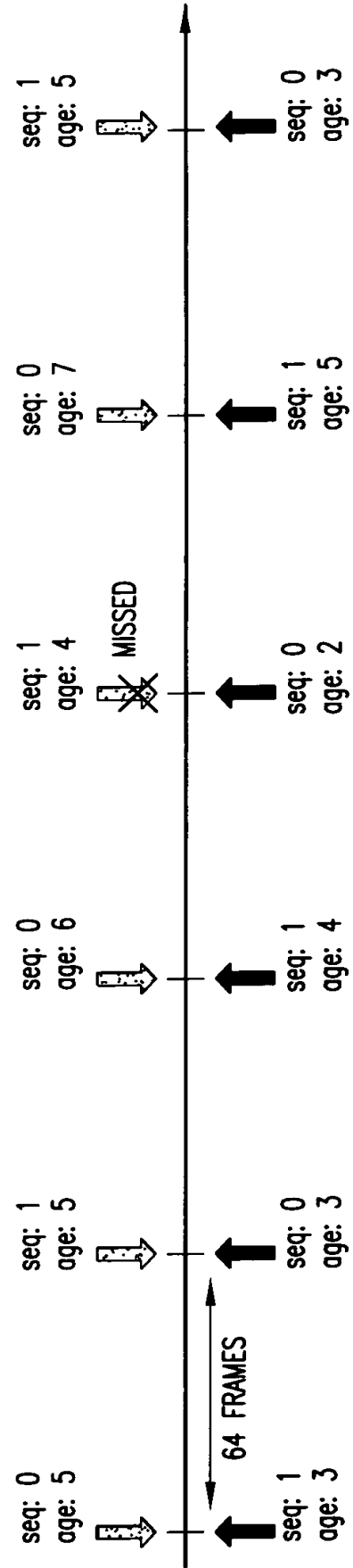


FIG. 8(a)

↓ CHECKPOINTS OF A TOWARD B
 ↑ CHECKPOINTS OF B TOWARD A



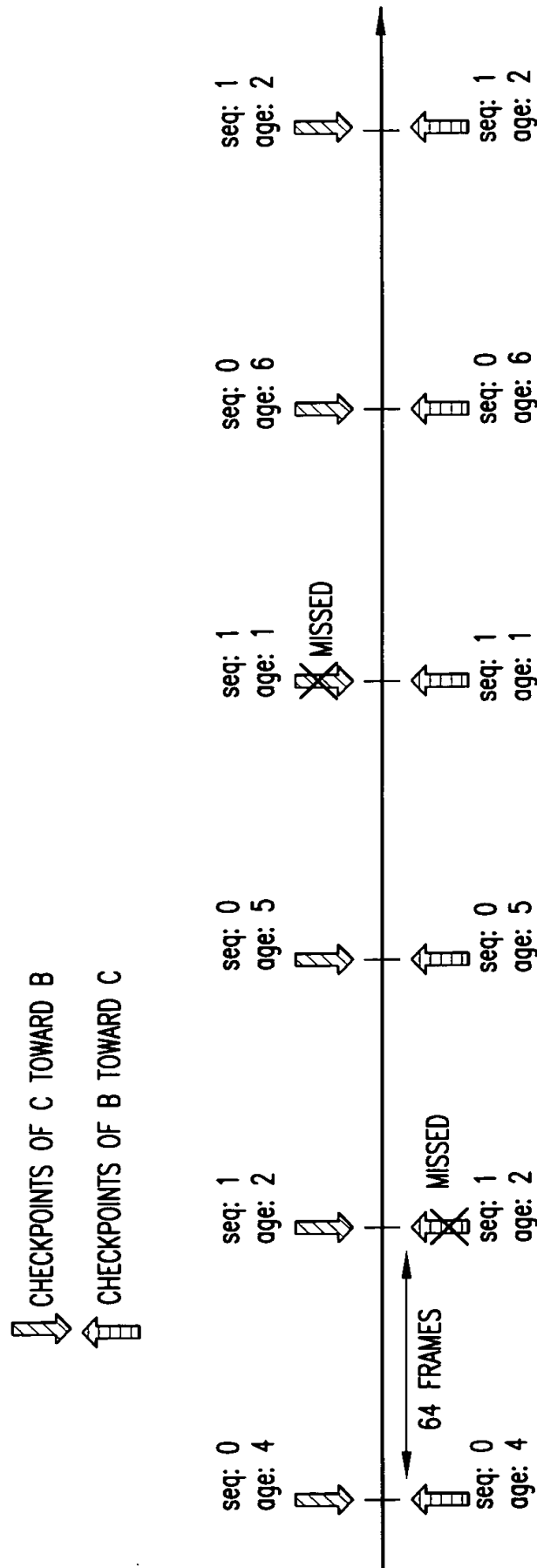


FIG.8(c)

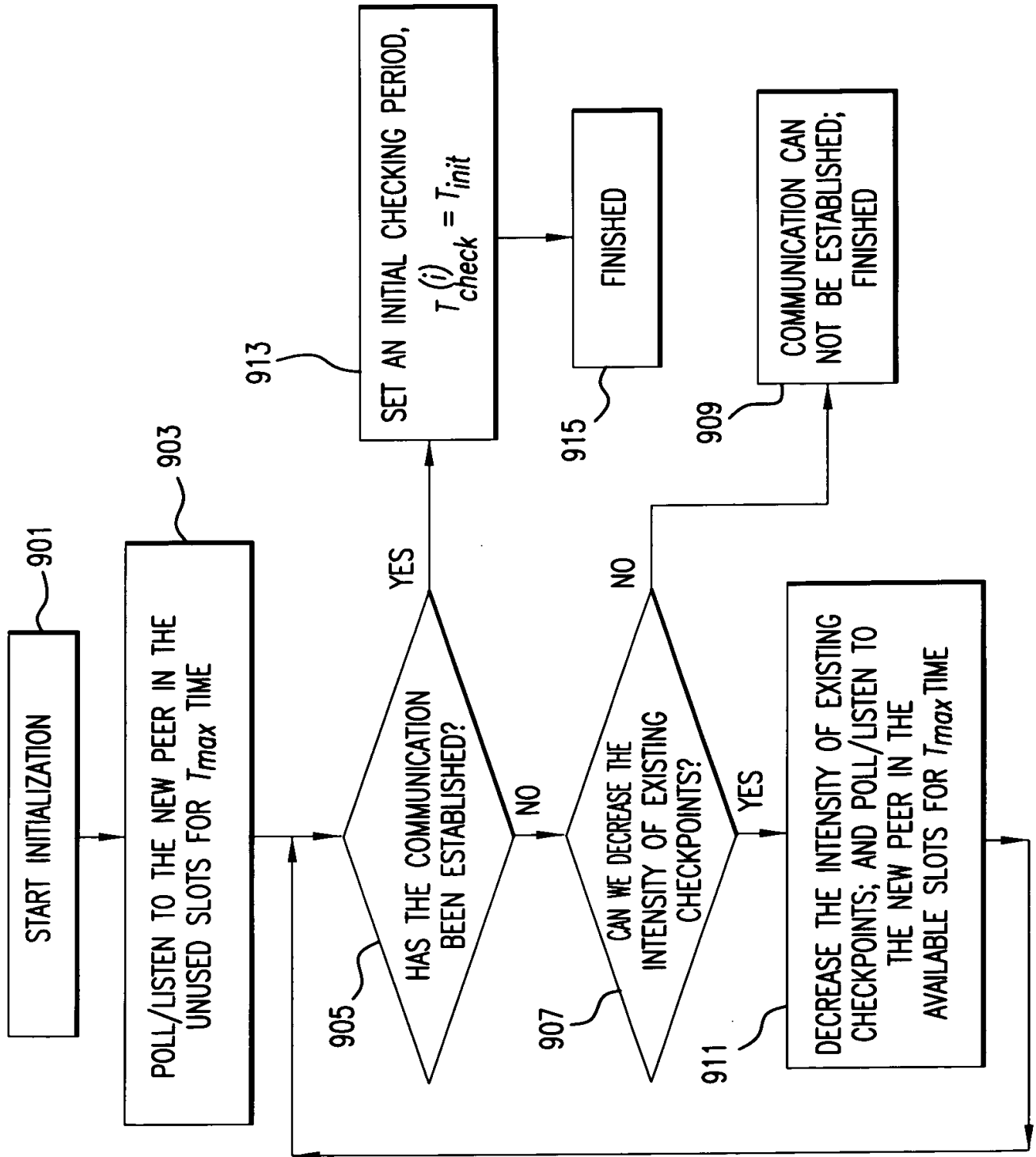


FIG. 9

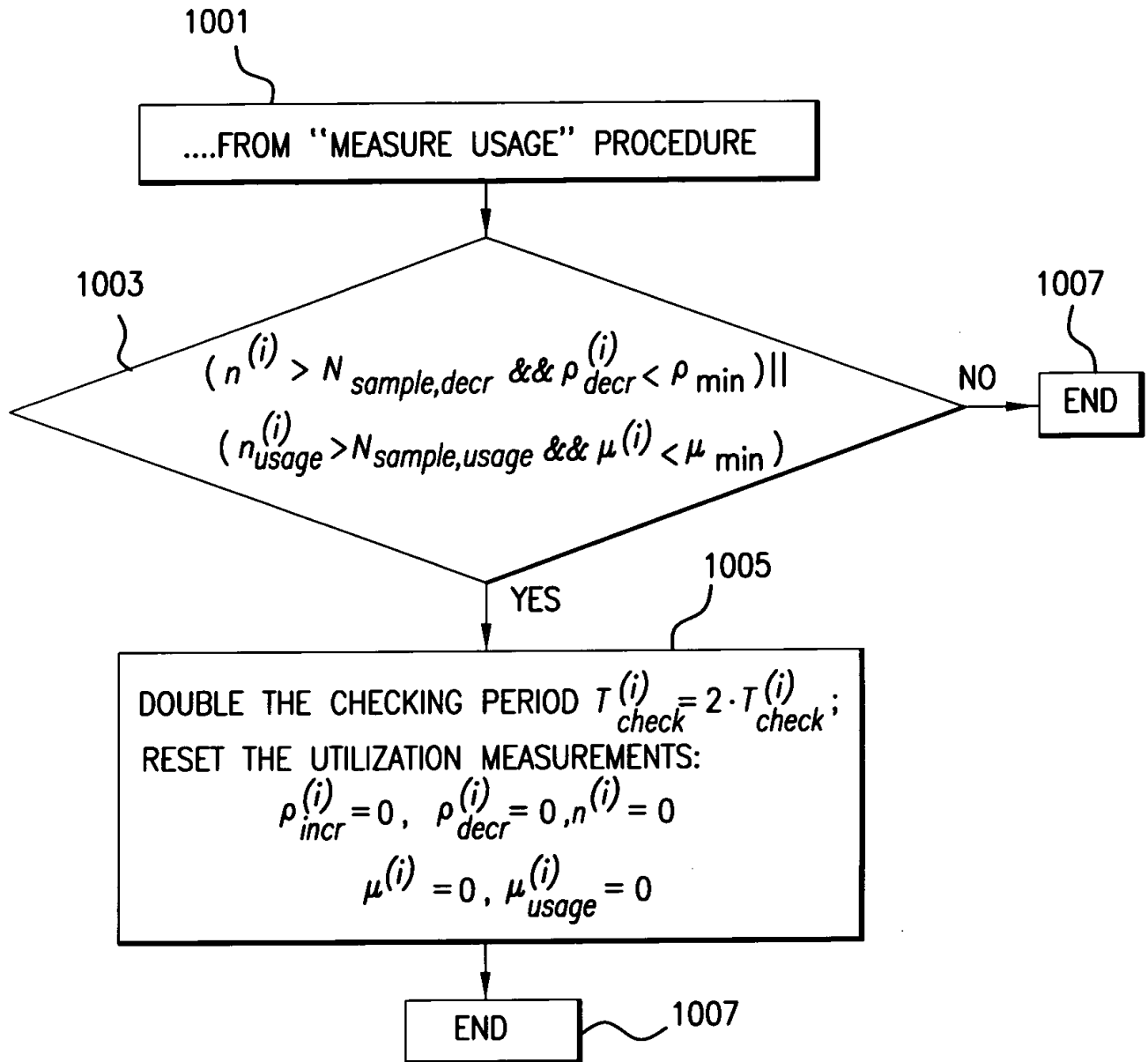


FIG.10

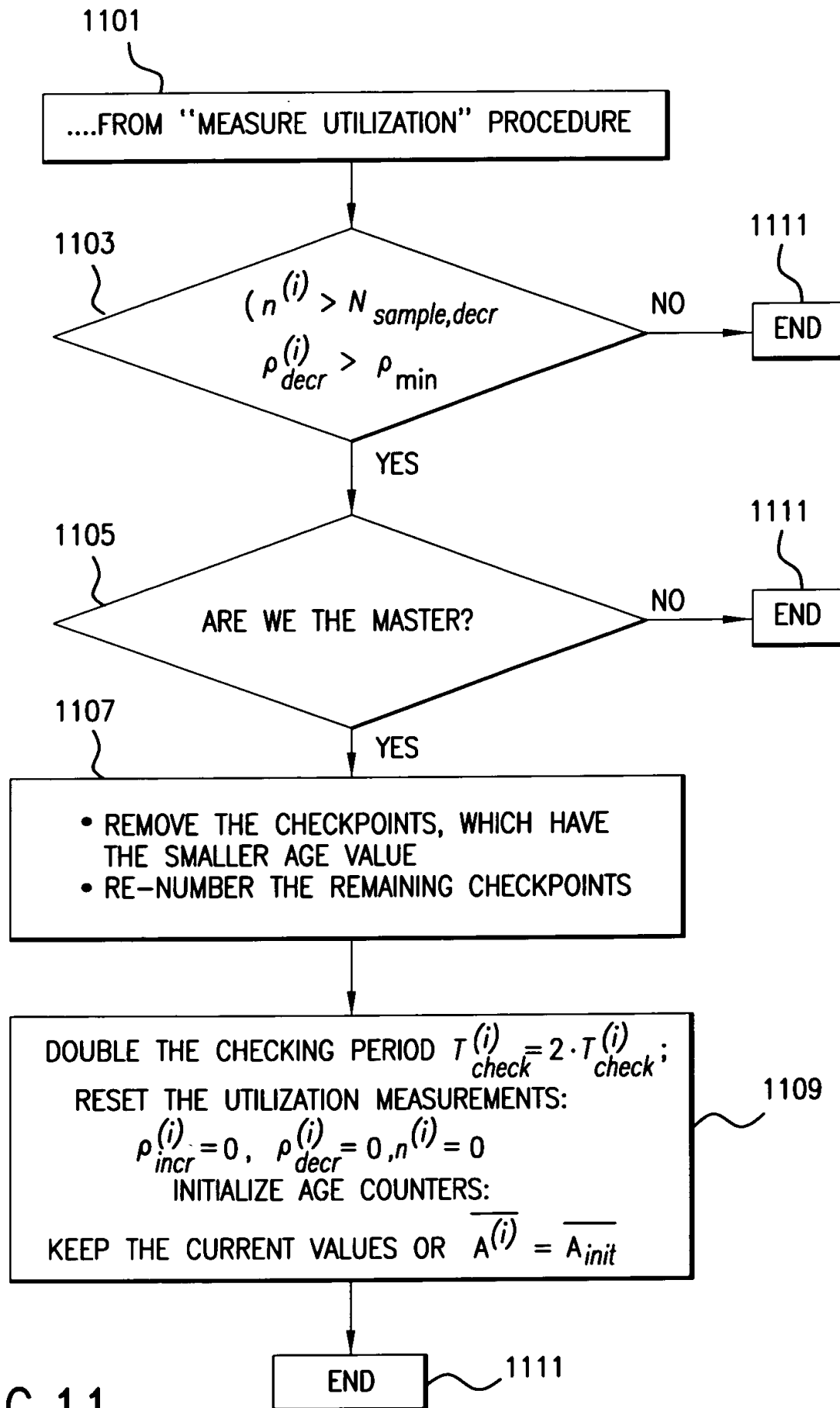


FIG.11

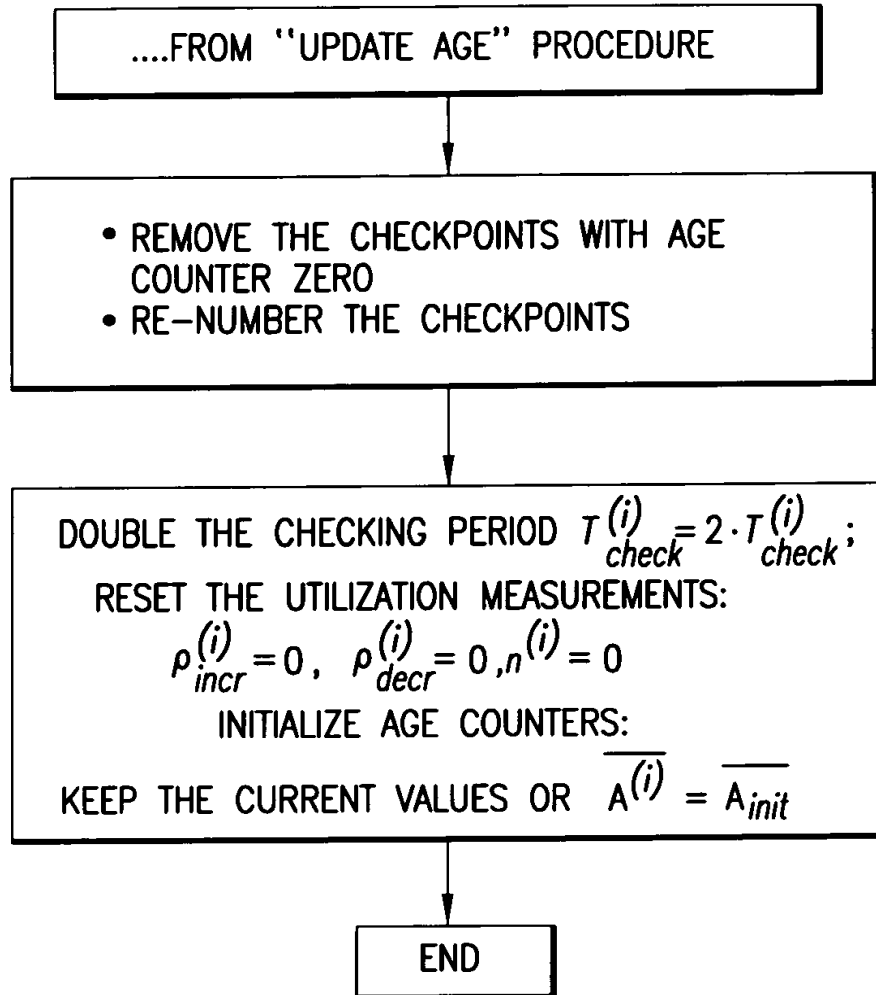


FIG.12

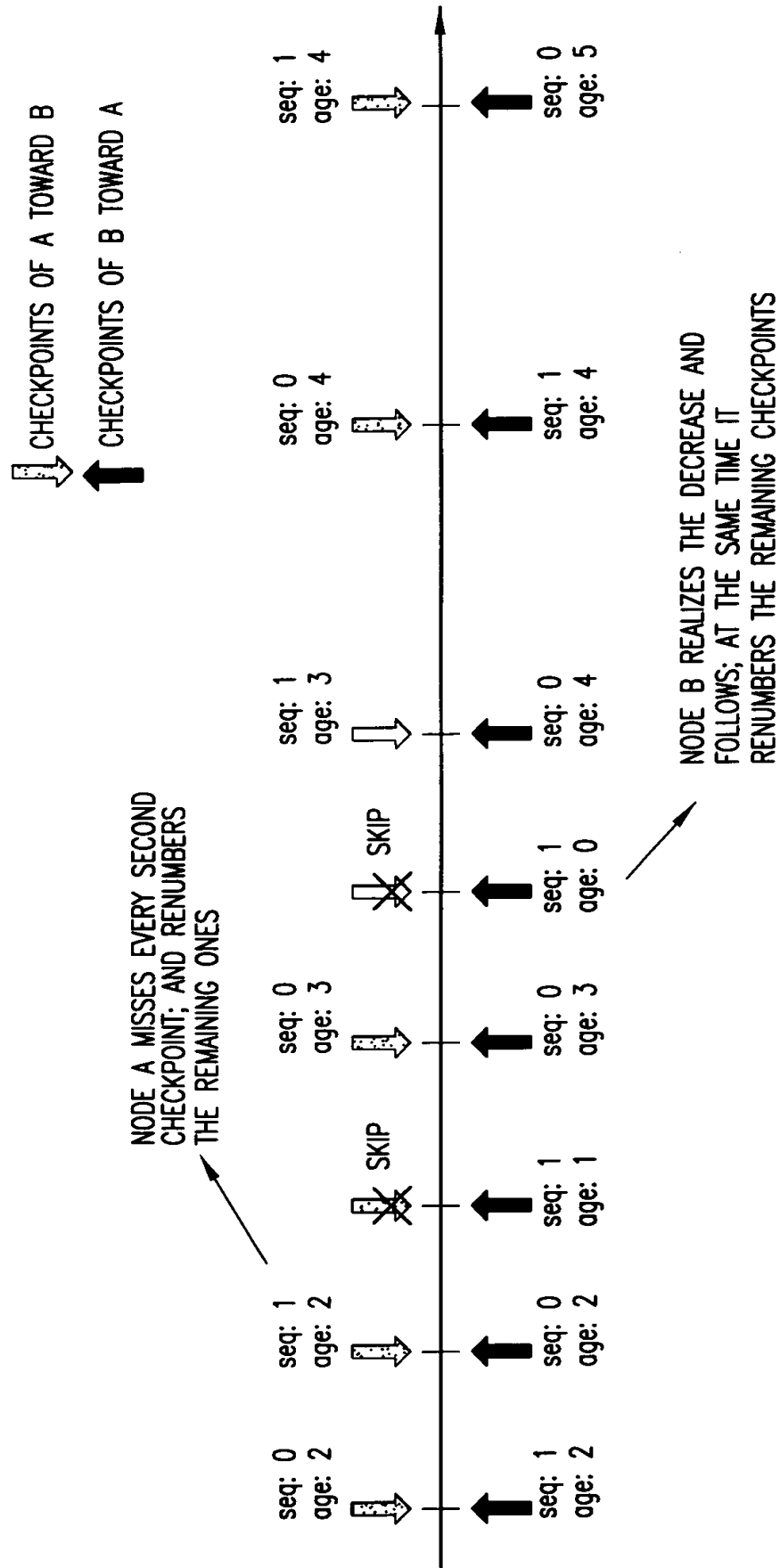


FIG.13(a)

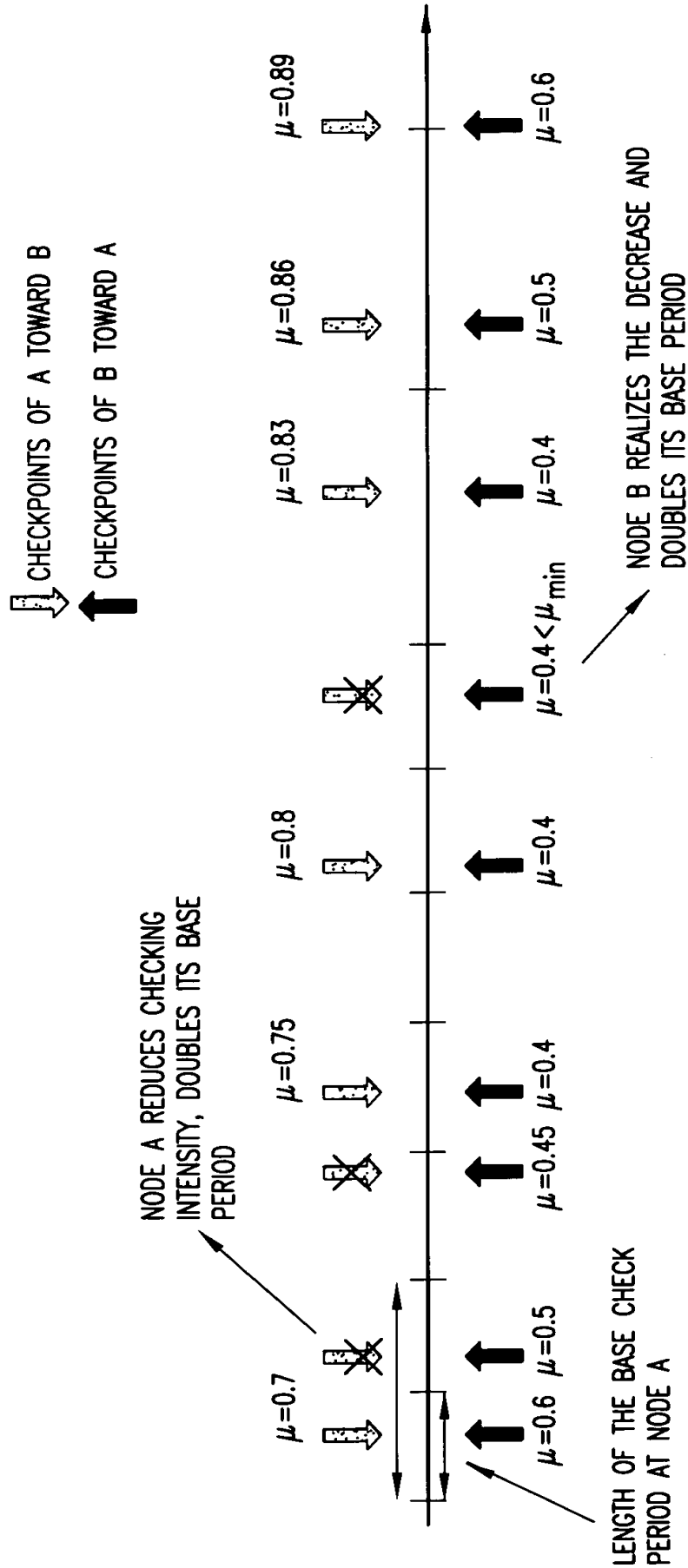
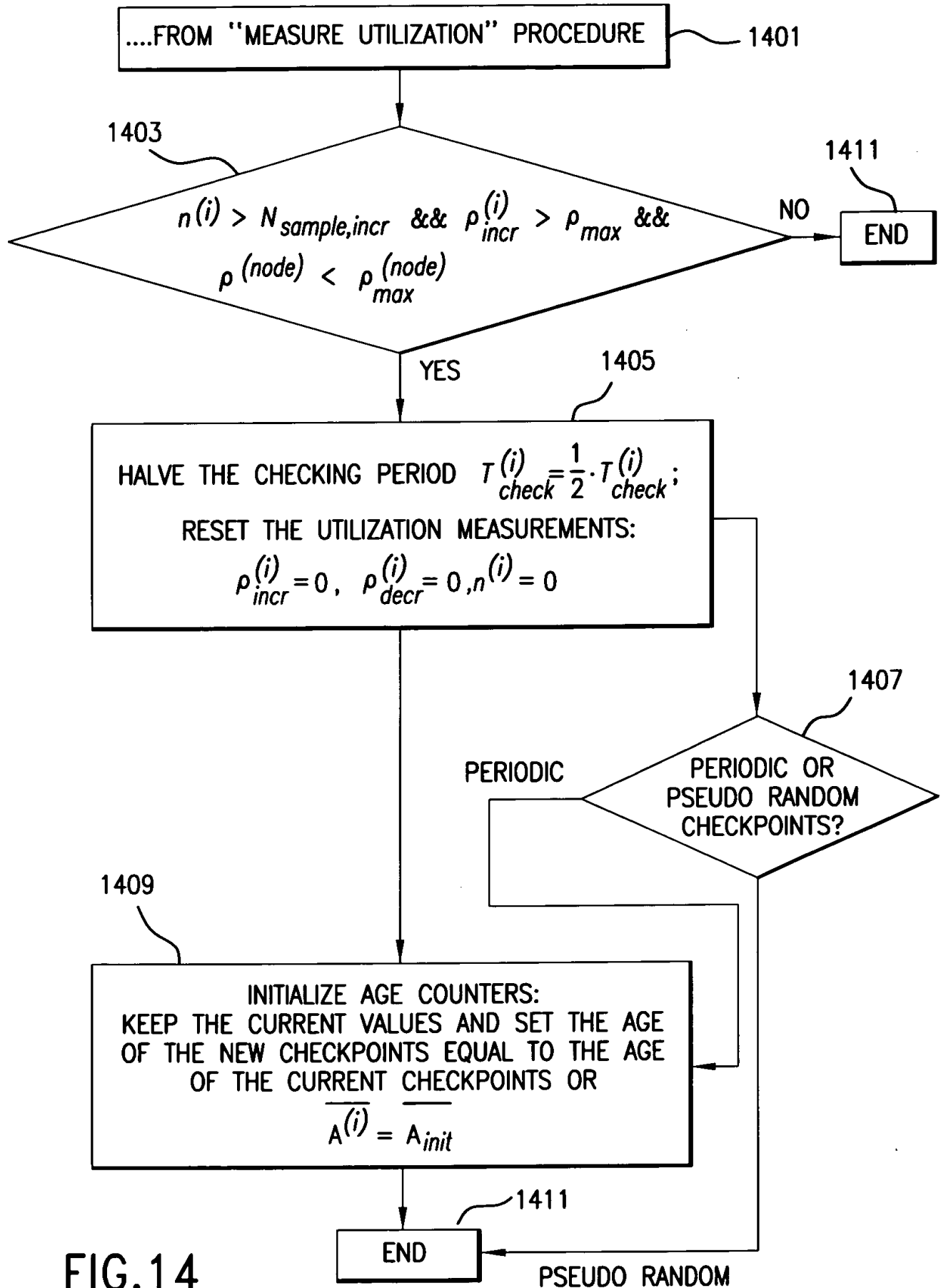


FIG.13(b)



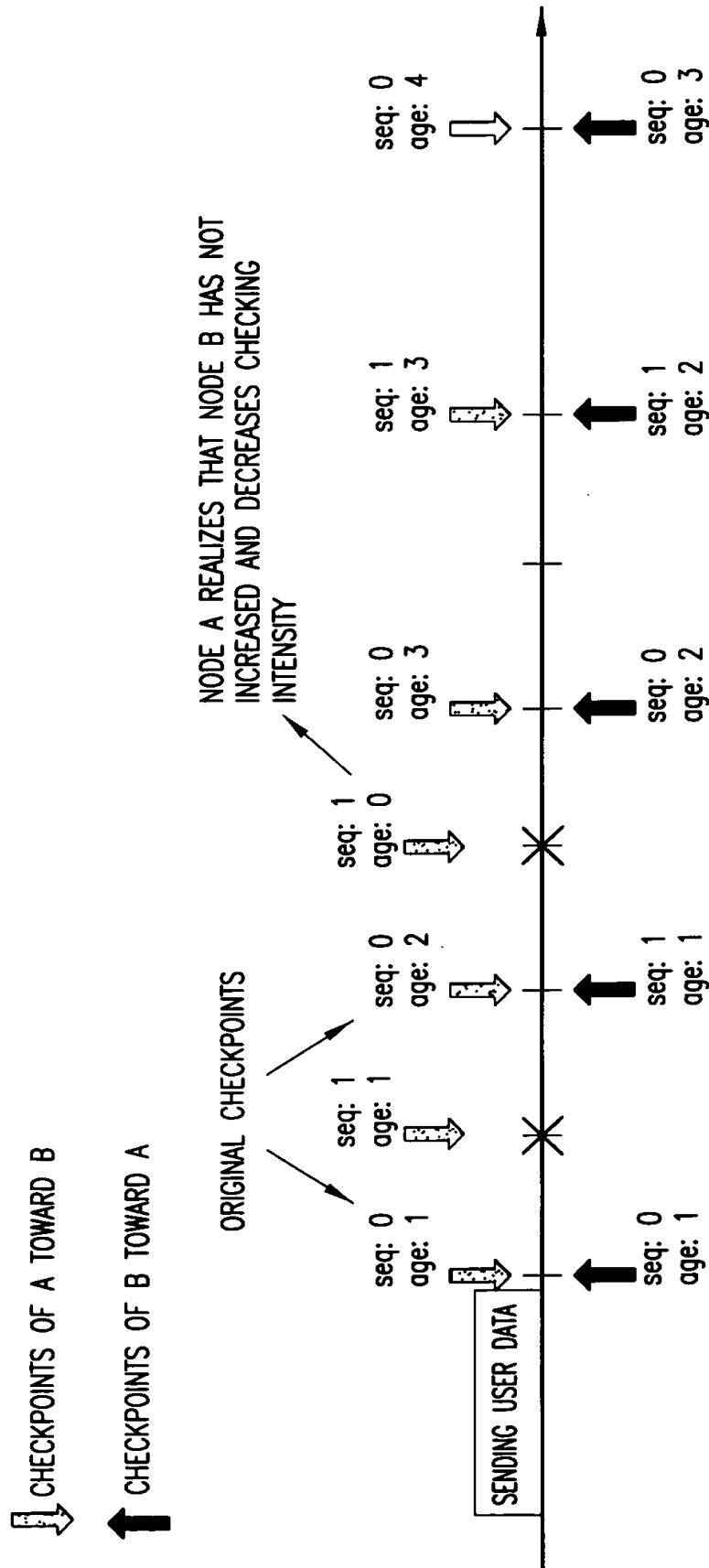
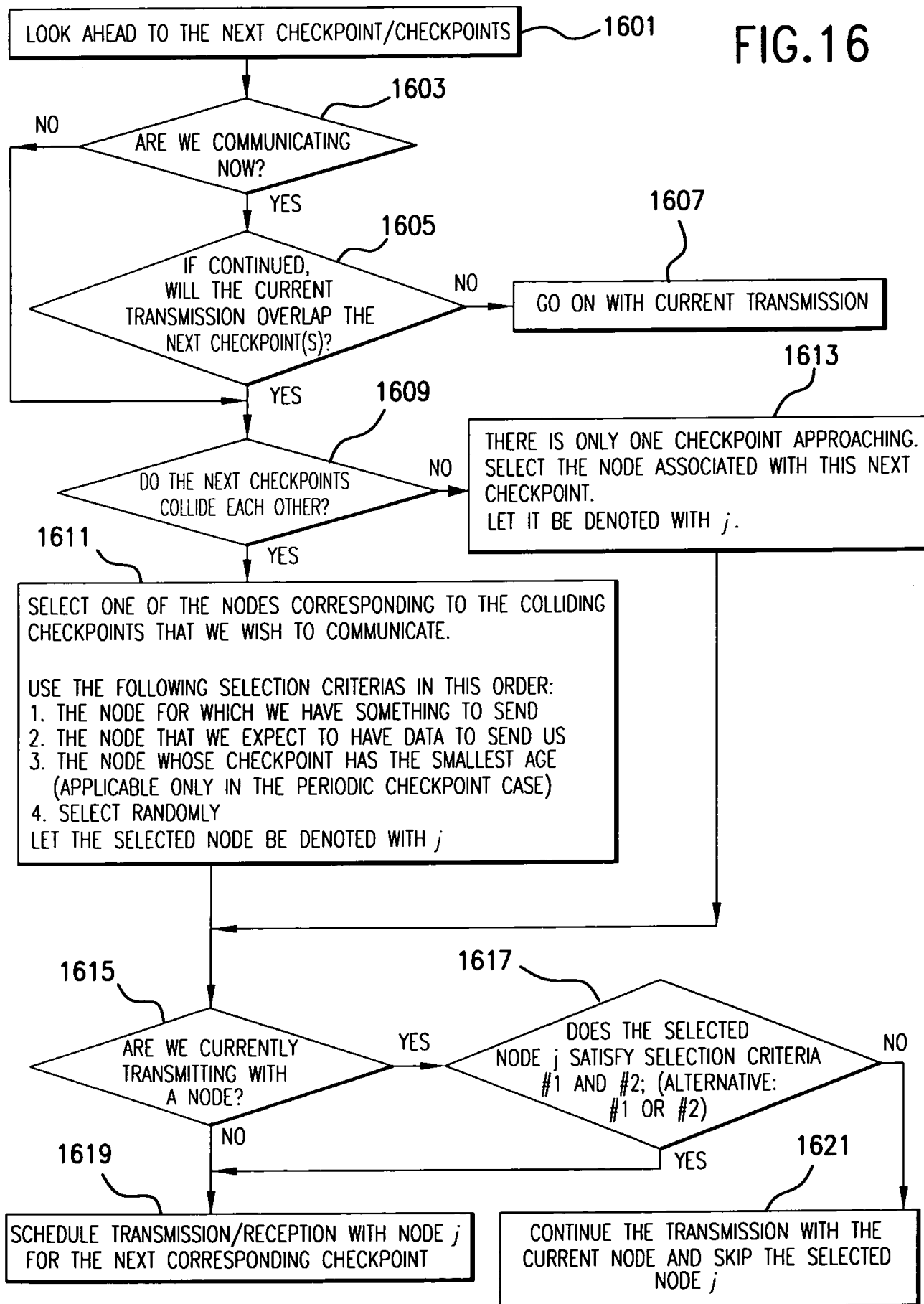


FIG.15

FIG.16



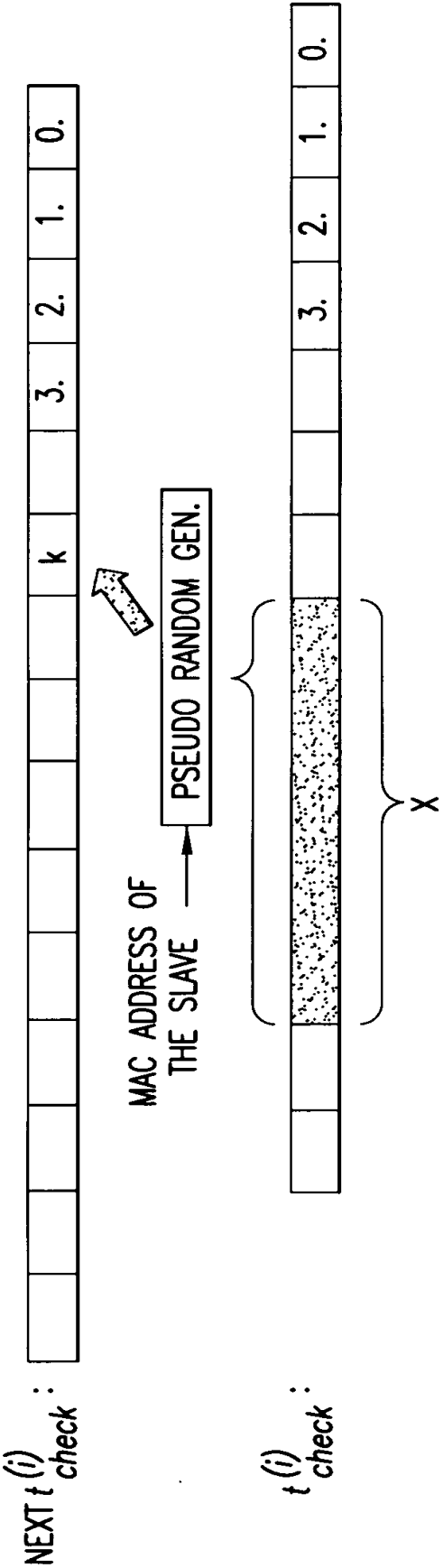


FIG.17

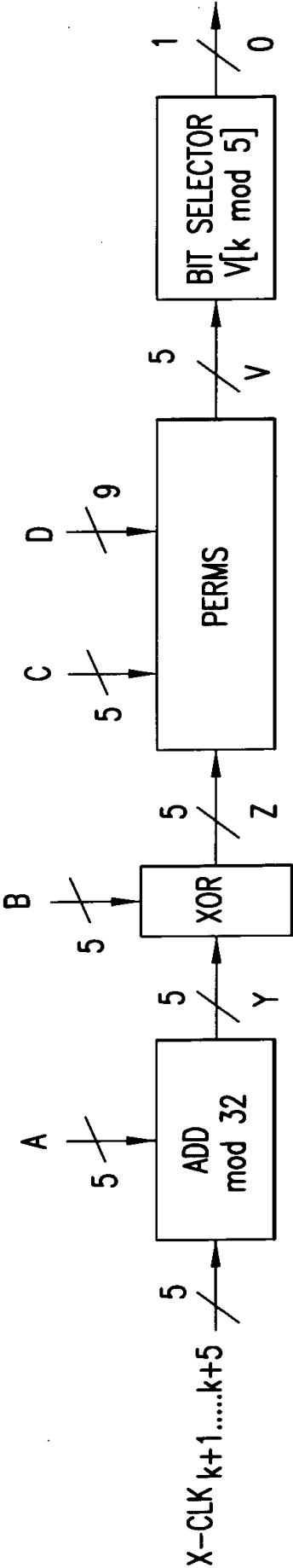


FIG.18

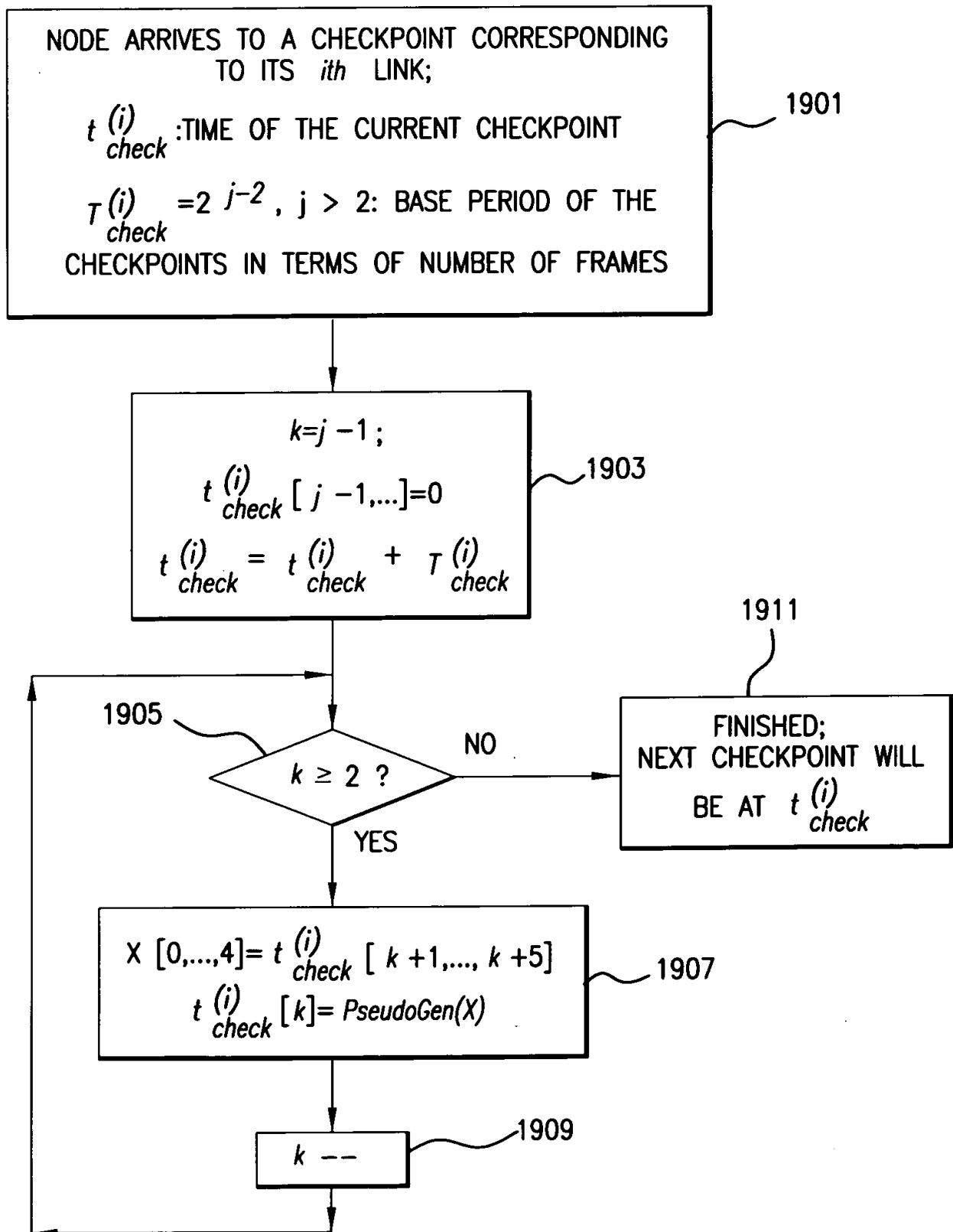


FIG.19

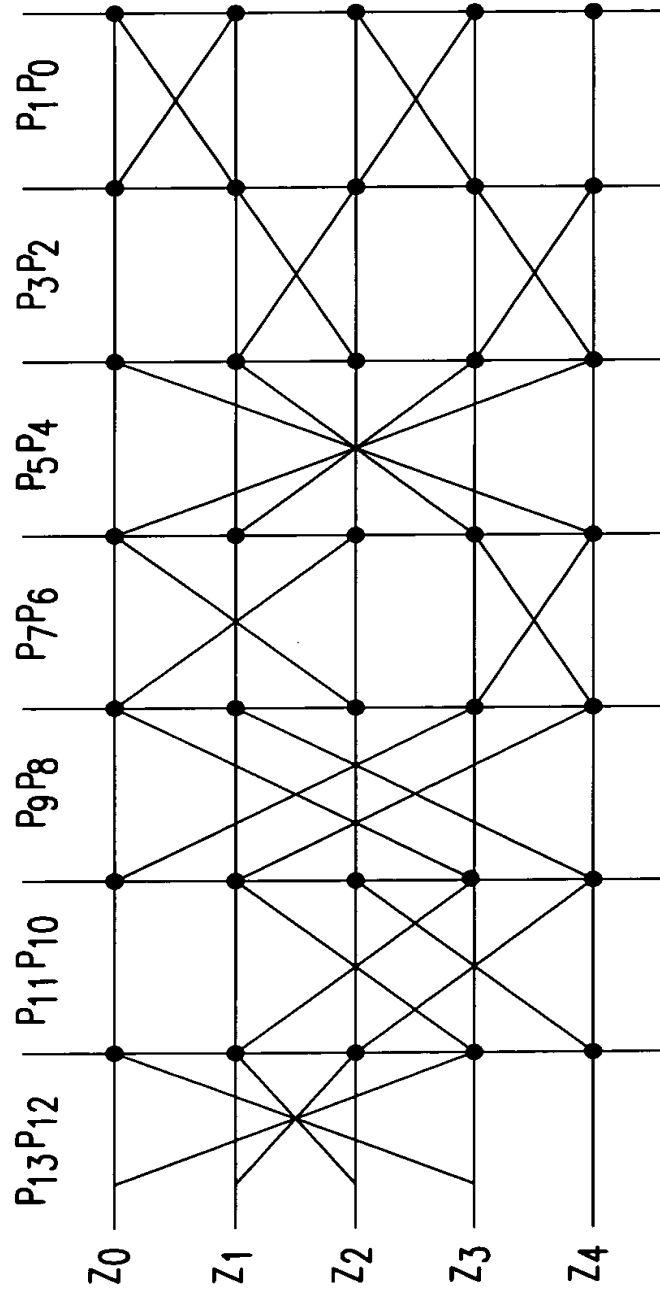


FIG.20

NODE ARCHITECTURE:

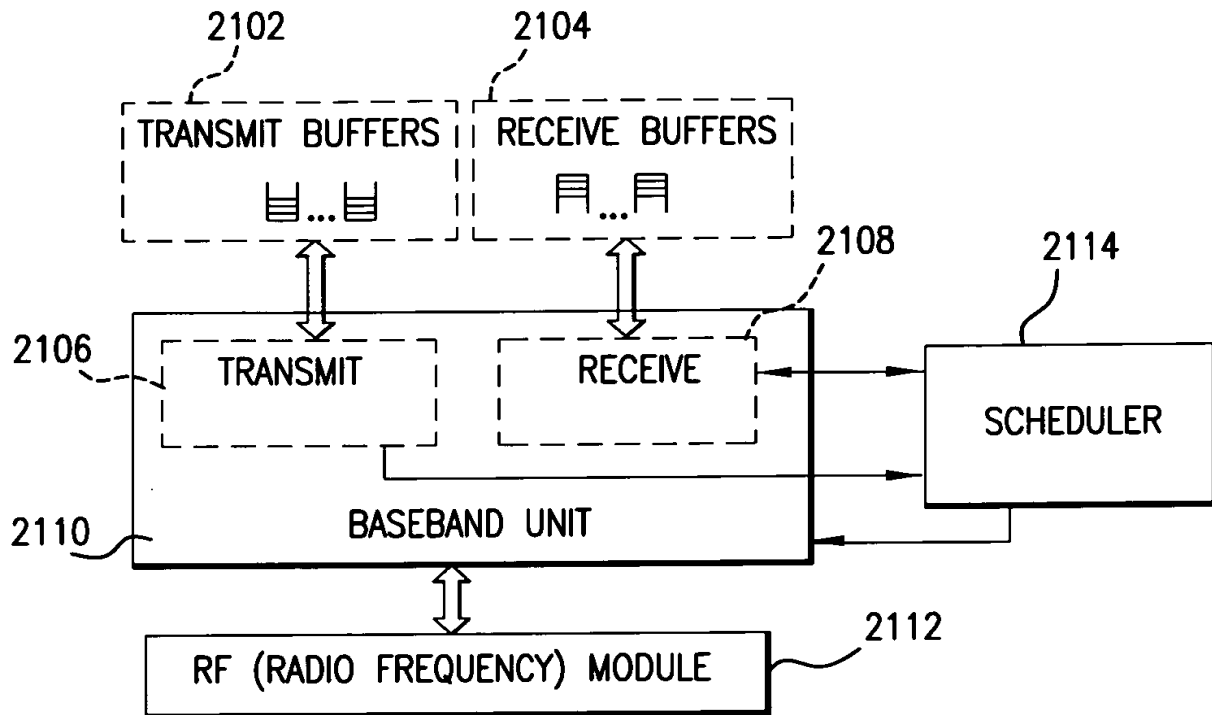


FIG.21(a)

ARCHITECTURE OF THE SCHEDULER:

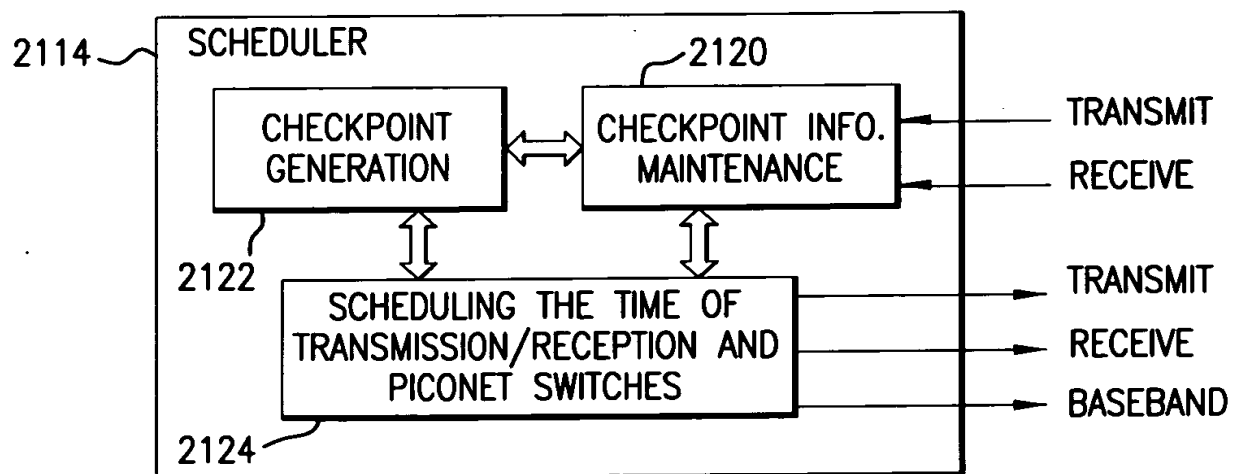


FIG.21(b)